

PTO-1449

SERIAL NO.
09/353,547

FILING DATE
07-04-99

GROUP
2734


[illegible]

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

16	"A Semidigital Dual Delay-Locked Loop" ; Stefanos Sidiropoulous; IEEE Journal of Solid-State Circuits, Vol. 32, No. 11, November 1997
16	"A 2Gb/s/pin CMOS Asymmetric Serial Link"; Chang et al; 1998 Symposium on VLSI Circuits Digest of Technical Papers

DATE CONSIDERED

8085 1449A.FRM (8/95)

 <p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				Complete if Known	
				Application Number	09/353,547
				Filing Date	July 14, 1999
				First Named Inventor	Sidiropoulos
				Art Unit	
				Examiner Name	
Sheet		of		Attorney Docket Number	9797-0022-999

[illegible][illegible]

Examiner Signature	<i>Karin Karin</i>	Date Considered	<i>5/6/03</i>
--------------------	--------------------	-----------------	---------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Standard Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.